

<b>Notice of References Cited</b>	Application/Control No. 09/886,585	Applicant(s)/Patent Under Reexamination LAVERY ET AL	
	Examiner Satish S. Rampuria	Art Unit 2191	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,974,538 A	10-1999	Wilmot, II, Richard Byron	712/218
	B	US-2002/0170034 A1	11-2002	Reeve et al.	717/127
	C	US-6,553,565 B2	04-2003	Click et al.	717/129
	D	US-6,668,372 B1	12-2003	Wu, Youfeng	717/130
	E	US-6,704,862 B1	03-2004	Chaudhry et al.	712/244
	F	US-6,732,084 B1	05-2004	Kabra et al.	707/2
	G	US-6,754,888 B1	06-2004	Dryfoos et al.	717/127
	H	US-6,834,364 B2	12-2004	Krech et al.	714/45
	I	US-2005/0086650 A1	04-2005	Yates et al.	717/139
	J	US-2005/0086451 A1	04-2005	Yates et al.	712/032
	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Corporaal, Design of transport triggered architectures, IEEE, 4-5 March 1994 Page(s):130 - 135
	V <sup>1</sup>	Baralis et al., Compile-time and runtime analysis of active behaviors, IEEE, Volume 10, Issue 3, May-June 1998 Page(s):353 - 370
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.